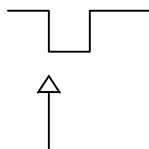
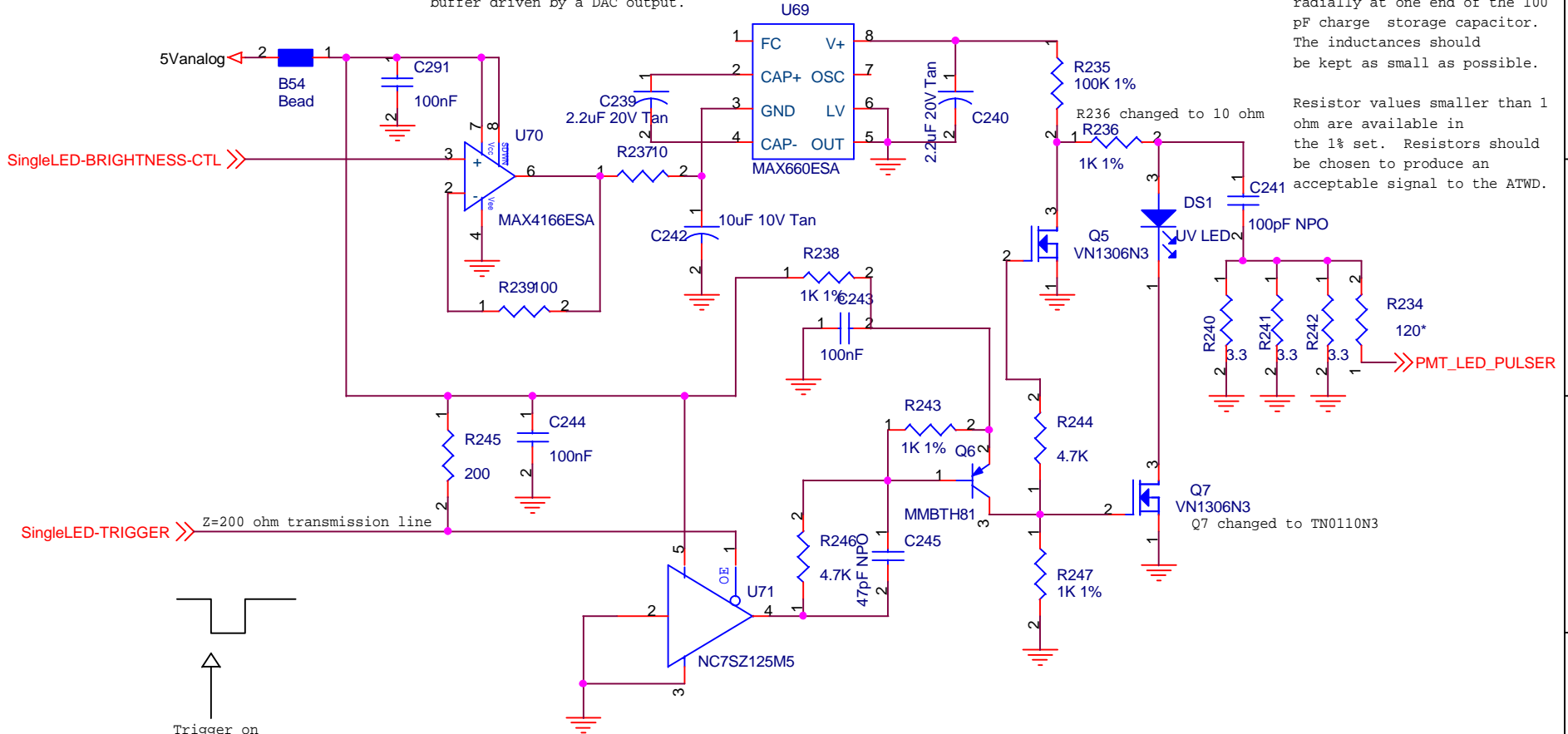


Charge pump voltage doubler driven by a buffer driven by a DAC output.

The resistors should be placed radially at one end of the 100 pF charge storage capacitor. The inductances should be kept as small as possible.

Resistor values smaller than 1 ohm are available in the 1% set. Resistors should be chosen to produce an acceptable signal to the ATWD.



Trigger on FALLING edge.
Pulse width >20 ns.

Title		
Digital Optical Module Single LED		
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