Tek Run: 2.50GS/s   Average

△: 52.0ns
@: 48.2ns

C2→C1 Dly
1.58ns
Low signal
amplitude

C2→C3 Dly
440ps
Low signal
amplitude

C2→C4 Dly
400ps
Low signal
amplitude

C3→C4 Dly
−20ps
Low signal
amplitude

gtp lbnl
8 Mar 2006
09:40:50

Ch1 2.00 V   Ch2 2.00 V   M 10.0ns Ch2 ∫ 600mV
Ch3 2.00 V   Ch4 2.00 V

CH1: 20 MHz ADC Clock on DOR card
CH2: 10 MHz Refference from GPS clock
CH3: 20 MHz ADC Clock from DOM A Under Test
CH4: 20 MHz ADC Clock from DOM B Under Test
Temperature -55C causes negative skew of DOM Clock