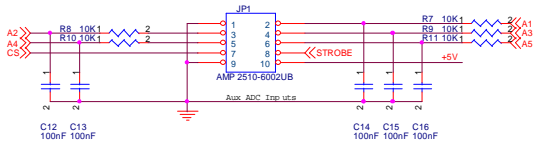


(vmax = 75., deltax = 63., vmin = 12., vstep = 1., R36 = 221K., R37R65 = 486200., Id = 4319e-4, R28R61 = 28940.)
 (R36 = 121000., vmax = 68., vstep = 1., deltax = 63., R37R65 = 363000., vmin = 5., R28R61 = 7202.380952)

The voltage steps might be too coarse to meet the IceCube requirements for setability.

PCB Stack-up:
 Routing Plane with 5 Mil logic signal traces
 7 Mil Laminate
 Ground Plane
 4 Mil Laminate
 Vdd Power Plane
 4 Mil Laminate
 Ground Plane
 10 Mil Laminate
 Ground Plane
 5 Mil Laminate
 Routing Plane with 5 Mil digital signal traces

Solder mask on top and bottom.
 Impedance of digital signal traces affected by copper thickness and soldermask thickness.



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